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| APPLICANTS Naoki Katoh, Ko | | | | | | |
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| This application 06/23/2000 PAT | f PCT/JP98/05688 12/ | 5 02/28/2002 PAT 6,7 16/1998 | 69,110 which is | a CON of | 09/582,327 | |
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| Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged Exar | STATE OR COUNTRY JAPAN | SHEETS DRAWING 18 | TOTAL CLAIMS 5 | | | |
| ADDRESS 24956 | | | | | | |
| TITLE Semiconductor integra semiconductor intergra | ted circuit device, stora ted circuit | nge medium on which | cell library is st | ored and de | esigning method for | |
| | | | | ☐ All Fees | | |
| RECEIVED No | | | 1.16 Fees (Filing) | | | |
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